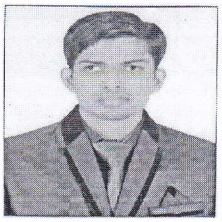
Preview of Application for Ph.D. in Electronics

Name	AJAY KUMAR VISVKARMA	**Category**	Unreserved	**Uploaded Files**
Gender	Male	**Nationality**	Indian	
Date of Birth	20-06-1991	**Age (As on 01-05-2017)**	24 Years 10 Month 12 Days	
Email	ajayvisvkarma13@gmail.com	**Mother/Father/Guardian's Name**	RAM KIRPAL VISVKARMA	
Mobile	9716222408	**University Last Attended**	University of Delhi	(http://admission.du.
Identity Proof	PAN Card	**ID Proof No.**	AQBPV1389E	**Passport**
PwD Category	Not Applicable	**Cw Category**	Not Applicable	
Writer Assistance Required	Not Applicable	**Postal Address**	C-92 , MOHAN GARDEN, UTTAM NAGAR West Delhi, Delhi - 110059 , India	

Department	Electronics	**Programme**	Ph.D.
Centre Choice	Delhi		

Educational Qualification

Examination Passed	Stream / Subject	Board / University	Year of Passing	Maximum Marks	Marks Obtained	Percentage
10+2 or Equivalent	Science	CBSE	2009	500	394	78.80

B.Sc (Hons)	ELECTRONICS	University of Delhi	2012	1500	1020	68.00
M.Sc	ELECTRONICS	University of Delhi	2014	1400	1012	72.29

Last College: DEPARTMENT OF ELECTRONIC SCIENCE,UDSC | **Last University:** University of Delhi | **Exam Roll Number (For DU Students only):** 1202

National Level Examination	Other (DRDO-SSPL)		
Title of Fellowship/Scholarship	**Certificate No.**	**Date**	**Fellowship Amount**
JRF UNDER PROJECT	1149	2016-11-28	32500
Other Details	Not Applicable		

Proposed theme and scope of research for M.Phil./Ph.D.

Gallium Nitride based HEMT devices.

Major writings in the field in which you would like to pursue your M.Phil./Ph.D.

Improvement in gate characteristics by reducing gate leakage and improving breakdown.

Primary sources/field work, methodology, hypothesis/research, questions and issues in the proposed field of interest.

Past Research Experience, Publications

Additional Information

For Refund of Fee

Name of Account Holder	AJAY KUMAR VISVKARMA	**Account Number**	235501000090	**Name of Bank**	ICICI	**IFSC Code**	ICIC0002355

Preview of Application for Ph.D. in Electronics

Name	Priyanka Pandey	**Category**	Unreserved	**Uploaded Files**
Gender	Female	**Nationality**	Indian	
Date of Birth	04-09-1992	**Age (As on 01-05-2017)**	24 Years 7 Month 28 Days	
Email	priyanka.pandey004@gmail.com	**Mother/Father/Guardian's Name**	O.N. Pandey	
Mobile	8447365517	**University Last Attended**	University of Delhi	(http://admissi
Identity Proof	Driving License	**ID Proof No.**	DL-0820100090544	**Passport**
PwD Category	Not Applicable	**Cw Category**	Not Applicable	
Writer Assistance Required	Not Applicable	**Postal Address**	CD-193 F.F. pitampura , Delhi North West Delhi, Delhi - 110034 , India	

Department	Electronics	**Programme**	Ph.D.
Centre Choice	Delhi		

Educational Qualification

Examination Passed	Stream / Subject	Board / University	Year of Passing	Maximum Marks	Marks Obtained	Percentage
10+2 or Equivalent	Science	CBSE	2010	500	433	86.60
B.Sc (Hons)	Electronics	University of	2013	3600	3137	87.14

		Delhi				
M.Sc	Electronics	University of Delhi	2015	1400	1222	87.29

Last College: Department of Electronic Science, South Campus/ University of Delhi	**Last University:** University of Delhi	**Exam Roll Number (For DU Students only):** 1316

National Level Examination	Not Applicable		
Title of Fellowship/Scholarship	**Certificate No.**	**Date**	**Fellowship Amount**
Other Details	Not Applicable		

Proposed theme and scope of research for M.Phil./Ph.D.

Device modeling and simulation, microelectronics

Major writings in the field in which you would like to pursue your M.Phil./Ph.D.

I would like to pursue my research in Device Modeling and Simulation.

Primary sources/field work, methodology, hypothesis/research, questions and issues in the proposed field of interest.

Past Research Experience, Publications

Additional Information

For Refund of Fee

Name of Account Holder	Priyanka Pandey	**Account Number**	8668101012541	**Name of Bank**	Canara bank	**IFSC Code**	CNRB0008668

Declaration

Preview of Application for Ph.D. in Electronics

Name	Monika Sharma	**Category**	Unreserved	**Uploaded Files**
Gender	Female	**Nationality**	Indian	
Date of Birth	14-04-1992	**Age (As on 01-05-2017)**	24 Years 0 Month 18 Days	
Email	ankit.sharma43102@gmail.com	**Mother/Father/Guardian's Name**	Pradeep Kumar Sharma	
Mobile	9716939367	**University Last Attended**	University of Delhi	(http://admission.d
Identity Proof	Voter's Identity Card	**ID Proof No.**	TPE2029337	**Passport**
PwD Category	Not Applicable	**Cw Category**	Not Applicable	
Writer Assistance Required	Not Applicable	**Postal Address**	1/7171 SHIVAJI PARK STREET NO. 3 , SHAHDARA North East Delhi, Delhi - 110032 , India	

Department	Electronics	**Programme**	Ph.D.
Centre Choice	Delhi		

Educational Qualification

Examination Passed	Stream / Subject	Board / University	Year of Passing	Maximum Marks	Marks Obtained	Percentage

10+2 or Equivalent	Science	CBSE	2010	500	419	83.80
B.Sc (Hons)	ELECTRONICS	University of Delhi	2013	3600	2994	83.17
M.Sc	ELECTRONICS	University of Delhi	2015	1400	1158	82.71

Last College: University of Delhi	**Last University:** University of Delhi	**Exam Roll Number (For DU Students only):** 1309

National Level Examination	UGC		
Title of Fellowship/Scholarship	**Certificate No.**	**Date**	**Fellowship Amount**
UGC-NET(LS)	152022308	2016-07-05	0
Other Details	Not Applicable		

Proposed theme and scope of research for M.Phil./Ph.D.

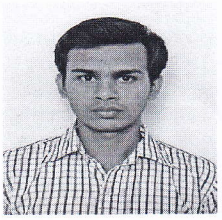
I would like to work on the devices such as MOSFET, Tunnel FETs for overcoming the short channel effects and to further use them for low power device applications, digital and analog applications. By using different gate engineering methods we can enhance its characteristics.

Major writings in the field in which you would like to pursue your M.Phil./Ph.D.

I have done the analytical modeling for Junctionless nanowire Transistor with arbitrary polygonal cross-section in which I have solved the Poisson's equation for the device and verified its results for electric potential and sub-threshold drain current with the ATLAS simulation software for square and circular cross-section. Among circular and square cross-section JNT, square cross-section junctionless nanowire shows better results as it is having lower sub-threshold current and it is also having higher noise margin that means it can be used for digital applications whereas in the case circular cross-section i.e. cylindrical device it require low threshold voltage that means it can used at low power applications and also as fast switches.

Semiconductor have crystal flaws, which caused by dangling bonds present at the interface. The presence of these defects centers, and traps, may significantly influence the characteristics of the device. Trap centers, whose associated energy lies in a forbidden gap, exchange charge with the conduction and valence bands through the emission and capture of electrons. The trap centers influence the density of space charge in

Preview of Application for Ph.D. in Electronics

Name	prashant chaudhary	**Category**	SC	**Uploaded Files**
Gender	Male	**Nationality**	Indian	
Date of Birth	23-09-1995	**Age (As on 01-05-2017)**	20 Years 7 Month 9 Days	
Email	prashantelec129@gmail.com	**Mother/Father/Guardian's Name**	Sheetal Prasad	
Mobile	7503787607	**University Last Attended**	University of Delhi	(http://admission.du.i
Identity Proof	Voter's Identity Card	**ID Proof No.**	NLN2028149	**Passport**
PwD Category	Not Applicable	**Cw Category**	Not Applicable	
Writer Assistance Required	Not Applicable	**Postal Address**	104, Sector - 9 , R.K.Puram South West Delhi, Delhi - 110022 , India	(http://admission.du.i

Department	Electronics	**Programme**	Ph.D.
Centre Choice	Delhi		

Educational Qualification

Examination Passed	Stream / Subject	Board / University	Year of Passing	Maximum Marks	Marks Obtained	Percentage
10+2 or Equivalent	Science	CBSE	2012	500	347	69.40
B.Sc (Hons)	Electronics	University of	2015	3600	2464	68.44

		Delhi				
M.Sc	Electronics	University of Delhi	2017	Result Awaited	Result Awaited	Not Applicable

Last College: University of Delhi	**Last University:** University of Delhi	**Exam Roll Number (For DU Students only):** 3620

National Level Examination	Not Applicable		
Title of Fellowship/Scholarship	**Certificate No.**	**Date**	**Fellowship Amount**
Other Details	Not Applicable		

Proposed theme and scope of research for M.Phil./Ph.D.

microwave antennas

Major writings in the field in which you would like to pursue your M.Phil./Ph.D.

Well Ideally the application is defined for the signal you are trying to process. It can be anything from audio, video, sensor output, data from the web, in short and simple words any sort of information. So processing it means making the information understandable i.e. like how discrete fourier transforms are used to understand the frequency components of a signal.

signal processing is widely use in filed of data compression, image video edit, and communication and audio processing.

Primary sources/field work, methodology, hypothesis/research, questions and issues in the proposed field of interest.

Audio signal processing is a highly active research field where digital signal processing theory meets human sound perception and real-time programming requirements. wide range of applications are their such as in computers, gaming, and music technology, etc. form a few to large one. Successful applications include for example perceptual audio coding, digital music synthesizers, and music recognition software. The fact that music is now often listened to using headphones from a mobile device leads to new problems related to background noise control and signal enhancement. Developments in processor technology, such as parallel computing, are changing the way signal-processing algorithms are designed for audio.

some issues that i want to talk about in signal processing

Preview of Application for Ph.D. in Electronics

Name	HINA YADAV	**Category**	Unreserved	**Uploaded Files**
Gender	Female	**Nationality**	Indian	
Date of Birth	03-10-1991	**Age (As on 01-05-2017)**	24 Years 6 Month 29 Days	
Email	hinayadav91@gmail.com	**Mother/Father/Guardian's Name**	RAKESH YADAV	
Mobile	8802865727	**University Last Attended**	University of Delhi	(http://admission.du.ac.
Identity Proof	Aadhar Card	**ID Proof No.**	708043898132	**Passport**
PwD Category	Not Applicable	**Cw Category**	Not Applicable	
Writer Assistance Required	Not Applicable	**Postal Address**	H no 739 SECTOR 21E , gurgaon Gurgaon, Haryana - 122016 , India	

Department	Electronics	**Programme**	Ph.D.
Centre Choice	Delhi		

Educational Qualification

Examination Passed	Stream / Subject	Board / University	Year of Passing	Maximum Marks	Marks Obtained	Percentage
10+2 or Equivalent	Science	CBSE	2009	500	333	66.60
B.tech	Electronics and	GBTU	2013	5000	3692	73.84

	communication					
M.Tech	Microwave Electronics	University of Delhi	2015	1800	1312	72.89

Last College: UNIVERSITY OF DELHI	**Last University:** University of Delhi	**Exam Roll Number (For DU Students only):** 1359

National Level Examination	Not Applicable		
Title of Fellowship/Scholarship	**Certificate No.**	**Date**	**Fellowship Amount**
Other Details	Not Applicable		

Proposed theme and scope of research for M.Phil./Ph.D.

RF MEMS reconfigurable multiple wide band Antenna for Spectrum Monitoring as Substrate resistivity

Major writings in the field in which you would like to pursue your M.Phil./Ph.D.

- 5 months internship at SAMEER (Society for Applied Microwave Electronics and Electrical Research), IIT Campus, Mumbai and worked on "**Investigation on Wire Antennas"** and designed monopole antenna (3 KHz -3 MHz) and hexagonal Loop Antenna (915 MHz).
- Design, Simulation, Fabrication and Measurement of **High Pass Filter** at 5 GHz using Microstrip Short stubs.
- A major project on design and simulation of multiband Chaucer fractal patch antenna loaded with dumbbell, fabricated on FR4 material having relative permittivity 4.4 and thickness 1.6 mm. The operating frequency is in the ISM band. The antenna resonates at 5.8 GHz.

Primary sources/field work, methodology, hypothesis/research, questions and issues in the proposed field of interest.

Past Research Experience, Publications

- Published a research paper titled "**Design and Simulation of Multiband Chaucer Fractal Patch Antenna Loaded with Dumbbell**" in International Journal of Electronics and Communication Engineering and Technology (IJECET), Volume 07, Issue 1.
- Two research papers communicated for ELSEVIER journal Titled, "Investigation of Hexagonal Loop Antenna" and "Active Monopole Antenna for Lightning Detection".

दिल्ली विश्वविद्यालय
University of Delhi

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Application for Ph.D. in Electronics

Amount	Transaction No.	Payment Date
₹ 500.00	201605305235621	2016-05-30 21:38:06.0

Department	Electronics	Programme	Ph.D.	Centre Choice	Delhi

Name	.monika bansal	**Category**	General	 Your Photo
Gender	Female	**Nationality**	Indian	
Date of Birth	27-09-1993	**Age (As on 01-05-2016)**	22 Years 7 Month 5 Days	
Email	monikabansal2709@gmail.com	**Mother/Father/Guardian's Name**	kushal bansal	
Mobile	8527941855	**University Last Attended**	University of Delhi	
Writer Assistance Required	Not Applicable	**Postal Address**	F-145 second floor sector-20 , near indian public school Gautam Buddha Nagar, Uttar Pradesh - 201301 , India	
Identity Proof	PAN Card	**ID Proof No.**	CEHPB3000G	**Passport** Not Applicable

Educational Qualification

Examination Passed	Subject/ Stream	Board/ University	Year	Maximum Marks	Marks Obtained	Percentage/CGPA
10+2	Science	RBSE	2010	650	609	93.69
B.Sc (Hons)	electronics	University of Delhi	2014	3600	3141	87.25
M.Sc	electronics	University of Delhi	2016	Result Awaited	Result Awaited	Not Applicable

Last College Attended:	Last Examination Roll Number (For DU Students only):
department of electronic science	3517

National Level Examination	UGC		
Title of Fellowship/Scholarship	**Certificate No.**	**Date**	**Fellowship Amount**
junior research fellowship	15028688	2016-04-11	25000
Other Details	Not Applicable		

Proposed theme and scope of research for M.Phil./Ph.D.
NOT APPLICABLE

Major writings in the field in which you would like to pursue your M.Phil./Ph.D.
NOT APPLICABLE

Primary sources/field work, methodology, hypothesis/research, questions and issues in the proposed field of interest.
NOT APPLICABLE

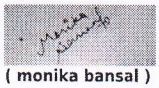
Past Research Experience, Publications
NOT APPLICABLE

Additional Information
NA

Uploaded Files

1. Photo 2. Signature 3. ID Proof 4. D.O.B. Certificate

Declaration
I have checked all the entries made by me in the form. Any wrong information given by me will lead to cancellation of my admission and also penal action against me.



(monika bansal)